


Applicant: Jae-Hun Kim and Kyu-Whan Chong
Serial No. not yet assigned
Filing Date: September 8, 2003 Group: not yet assigned
Title: MOS TRANSISTOR HAVING A RECESSED GATE ELECTRODE AND
FABRICATION METHOD THEREOF

**INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)**

U.S. PATENT DOCUMENTS

Exa m Init	<u>Ref</u>	Document <u>Number</u>	Issue <u>Date</u>	<u>Name</u>	<u>Class</u>	Sub <u>Class</u>
	_____	6,358,800 B1	3/19/2002	Tseng		

FOREIGN PATENT DOCUMENTS

Exa m Init	<u>Ref</u>	Document <u>Number</u>	Publication <u>Date</u>	<u>Country</u>	<u>Name</u>
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OTHER DOCUMENTS

Exam Init	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, Etc.)</u>
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Examiner:



Date Considered:

Sept 12 2004